

STANDARD CHARACTERISTICS OF PASSIVE AND ACTIVE DELAY LINES

The specifications below apply to all passive and active delay lines listed in this catalog, unless otherwise specified:

ELECTRICAL CHARACTERISTICS — PASSIVE LINES

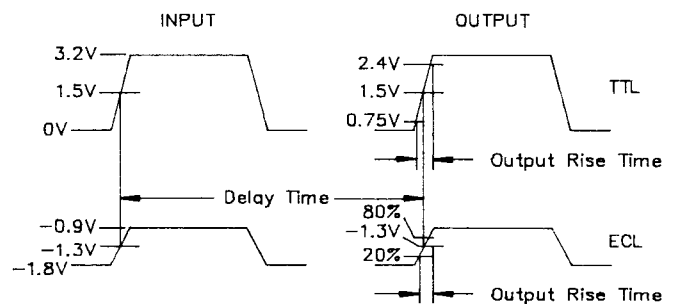
Operating Temperature: 0 C to +70 C
Storage Temperature: -55 to +125 C
T.C. of Delay: 100 ppm/ C
Insulation Resistance: 1 G MIN/100 VDC

ELECTRICAL CHARACTERISTICS — ACTIVE LINES

Operating Temperature: 0 C to +70 C
Storage Temperature: -55 C to +125 C
Input and output current and voltage specifications for TTL devices are standard Schottky values, and for ECL devices are standard 10K ECL values.

STANDARD TEST CONDITIONS FOR ACTIVE DELAY NETWORKS

- Measurements made at 25 ± 5 C.
- $V_{CC} = 5.0 \pm 0.1$ VDC (TTL),
 $V_{EE} = -5.2 \pm 0.1$ VDC (ECL).
- No load on outputs (TTL), 330 to Vee (ECL).
- Delay time measurements made, on positive edge only, at 1.5 V level (TTL), -1.3 V level (ECL).
- Input pulse voltage: 3.2 V (TTL), 0.9 V (ECL)
- Input pulse rise time: 3 ± 0.5 ns
(10% -90%)
- Input pulse width: 2.5 x Total Delay TYP
- Input pulse duty cycle: 20% TYP



MECHANICAL CHARACTERISTICS — ACTIVE AND PASSIVE LINES

Body Material: Black epoxy or D.A.P.
Lead Material: Kovar or Alloy 42, or equivalent.
Tin plated to pass solderability per MIL-STD-202, Method 208.