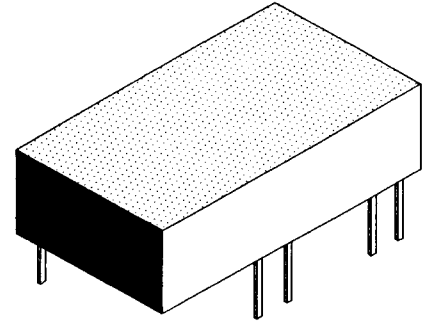


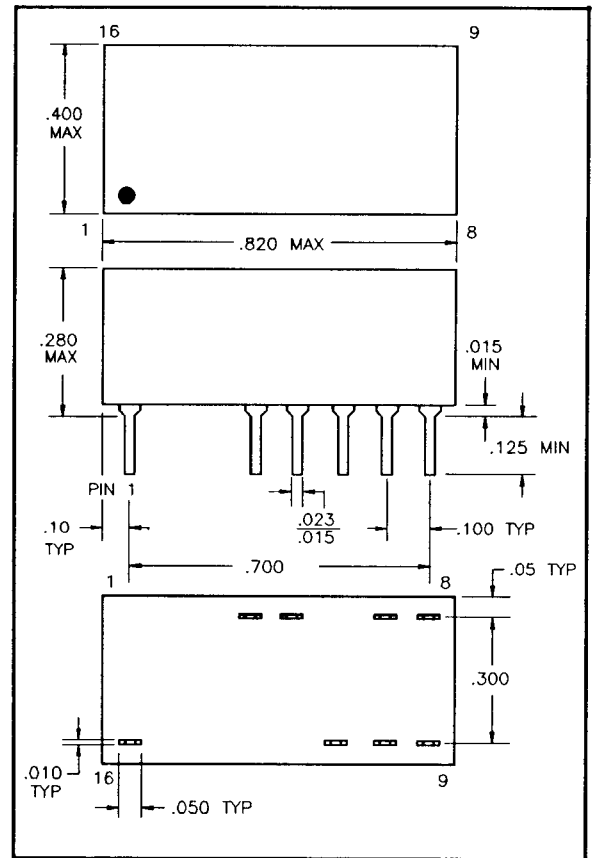
16 PIN, 3 BIT PROGRAMMABLE, TTL ACTIVES

DC Electrical Characteristics		Test Conditions	Min	Max	Unit
V _{OH}	High-Level Output Voltage	V _{CC} =min. V _L =max. I _{OH} =max	2.7		V
V _{OL}	Low Level Output Voltage	V _{CC} =min. V _{IH} =min. I _{OL} =max		0.5	V
V _{IK}	Input Clamp Voltage	V _{CC} =min. I _I =-18mA		-1.2	V
I _{IH}	High-Level Input Current	V _{CC} =max. V _{IN} =2.7V		10	uA
I _{IL}	Low-Level Input Current	V _{CC} =max. V _{IN} =5.25V		15	mA
I _{OS}	Short Circuit Output Current	V _{CC} =max. V _{OUT} =0 (One output at a time)	-40	-100	mA
I _{CCH}	High-Level Supply Current	V _{CC} =max. V _{IN} =OPEN		45	mA
I _{CCL}	Low-Level Supply Current	V _{CC} =max. V _{IN} =0		90	mA
T _{RO}	Output Rise Time	T _d ≤ 500nS(0.75 to 2.4 Volts)		4	nS
N _H	Fanout High-Level Output	V _{CC} =max. V _{OH} =2.7V		20 TTL LOAD	
N _L	Fanout Low-Level Output	V _{CC} =max. V _{OH} =0.5V		10 TTL LOAD	



Recommended Operating Conditions		Min	Max	Unit
V _{CC}	Supply Voltage	4.75	5.25	V
V _{IH}	High-level Input voltage	2.0		V
V _{IL}	Low-Level Input Voltage		0.8	V
I _{OH}	High-Level Output Current		-1.0	mA
I _{OL}	Low-Level Output Current		20	mA
PW	Pulse Width % of Total Delay	100		%
T _A	Operating Free-Air Temperature	0	+70	°C

Input Pulse Test Conditions @ 25°C		Unit
E _{IN}	Pulse Input Voltage	3.2 Volts
PW	Pulse Width % of Total Delay	150 %
T _{RI}	Pulse Rise Time(0.75 - 2.4 Volts)	2.0 nS
PRR	Pulse Repetition Rate @ T _d ≤ 500 nS	1.0 MHz
V _{CC}	Supply Voltage	5.0 Volts



MIN DELAY (Inherent)	MAX DELAY (Nom)	DELAY/STEP (Ref. Inherent Delay)	POLARA P/N AXT - PINOUT
7 ± 2	14	1 ± .5	AXT-0014
7 ± 2	21	2 ± .5	AXT-0021
7 ± 2	28	3 ± .5	AXT-0028
7 ± 2	35	4 ± 1	AXT-0035
7 ± 2	42	5 ± 1	AXT-0042
7 ± 2	49	6 ± 1	AXT-0049
7 ± 2	56	7 ± 1	AXT-0056
7 ± 2	63	8 ± 1	AXT-0063
7 ± 2	70	9 ± 1	AXT-0070
7 ± 2	77	10 ± 1.0	AXT-0077

